



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chow, Chung-kai; De Angel, Edwin; Lee, Wai Laing; Page, Joel;
Wang, Lei; Zheng, Hong
Assignee: Cirrus Logic, Inc.
Title: Power On Reset Techniques For An Integrated Circuit Chip
Serial No.: 09/153,864 Filing Date: September 16, 1998
Examiner: J. Zweizig Group Art Unit: 2816
Docket No.: M-11434 US

San Jose, California
October 17, 2001


COMMISSIONER FOR PATENTS
Washington, D. C. 20231

**SUBMISSION OF POWER OF ATTORNEY BY ASSIGNEE OF
ENTIRE INTEREST**

Dear Sir:


Transmitted herewith is a true and correct copy of the Power of Attorney by
Assignee of Entire Interest for the above-identified application. The original Power of
Attorney was filed in another pending application of the Assignee, Cirrus Logic, Inc.,
specifically Application Serial No. 09/865,079.

I hereby certify that this correspondence is being deposited with
the United States Postal Service as First Class Mail in an envelope
addressed to: Commissioner for Patents, Washington, D.C. 20231,
on October 17, 2001.


Attorney for Applicant(s)

10/17/01
Date of Signature

Respectfully submitted,


Marc R. Ascolese
Attorney for Applicant(s)
Reg. No. 42,268

RECEIVED
JAN 28 PM 12:31
LAW OFFICES OF
SKJERNEN MORRILL
AND INTERESTS

LAW OFFICES OF
SKJERNEN MORRILL
MACPHERSON LLP

25 METRO DRIVE
SUITE 700
SAN JOSE, CA 95110
(408) 453-9200
FAX (408) 453-7979



October 04, 2001

COMMISSIONER FOR PATENTS
Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST

Sir:

Cirrus Logic, Inc., assignee, hereby revokes all powers of attorney previously given and appoints hereby the following attorney(s) and/or agent(s) to prosecute the applications identified on Exhibit A attached hereto and to transact all business in the United States Patent and Trademark Office in connection therewith:

Michael P. Adams (34,763); Christopher B. Allenby (45,906); Marc R. Ascolese (42,268); Shireen Irani Bacon (40,494); Mary Jo Bertani (42,321); Todd M. Briggs (44,040); Matthew J. Brigham (44,047); Sam G. Campbell (42,381); Tom Chen (42,406); Alex Chen (45,591); Michael G. Cleveland (46,030); Carmen C. Cook (42,433); K. Alison De Runtz (37,119); David G. Dolezal (41,711); Gary J. Edwards (41,008); Rahul Engineer (47,548); Jason Far-Hadian (42,523); Jonathan Geld (44,702); Michael J. Halbert (40,633); Jonathan W. Hallman (42,622); David W. Heid (25,875); Joseph K. Hollinger (40,649); Signe Holmbeck (46,484); David C. Hsia (46,235); Judith S. Hom (44,772); Jame S. Hsue (29,545); Peter H. Kang (40,350); Margaret M. Kelton (44,182); Do Te Kim (46,231); Norman R. Klivans (33,003); Ken John Koestner (33,004); Edward C. Kwok (33,938); Don C. Lawrence (31,975); Rachel V. Leiterman (46,868); Paul E. Lewkowicz (44,870); Elaine H. Lo (41,158); Theodore P. Lopez (44,881); Alan H. Macpherson (24,423); Fabio E. Marino (43,339); Ronald J. Meetin (29,089); Gregory J. Michelson (44,940); Robert B. Morrill (43,817); Jack V. Musgrove (31,986); John A. Odozynski (28,769); Brian D. Ogonowsky (31,988); Daniel W. Ortman (43,356); Gerald P. Parsons (24,486); James E. Parsons (34,691); Gregory T. Powell (47,005); D'Ann Rifai (47,026); Emmanuel Rivera (45,760); William L. Robinson (47,032); Mark J. Rozman (42,117); Roberta P. Saxon (43,087); Brian A. Schar (45,076); Mark E. Schmidt (47,063); Michael Shenker (34,250); Matthew J. Spark (43,453); Eric A. Stephenson (38,321); David E. Steuber (25,557); Daniel P. Stewart (41,332); Robert Strawbrich (36,692); Omkar K. Suryadevara (36,320); Stephen A. Terrile (32,946); Robert D. Wasson (40,218); Rita M. Wisor (41,382); George Wolken, Jr. (30,441); Philip W. Woo (39,880); Philip Yau (32,892); Gergely T. Zimanyi (45,754); Abdul R. Zindani (46,901), and Scott Thomas (39,855); Dan A. Shifrin (34,473); and Steven Lin (35,250).

Please address all correspondence and telephone calls regarding the applications identified on Exhibit A to:

RECEIVED
JAN 22 2002
TECHNOLOGY CENTER 2800
RECEIVED
JUN 28 PM 12:32
BOARD OF PATENT APPEALS
AND INTERFERENCES

Jack V. Musgrove
SKJERVEN MORRILL MacPHERSON LLP
25 Metro Drive, Suite 700
San Jose, California 95110

ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(B)

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title and interest in the patent applications identified on Exhibit A by virtue of a chain of title from the inventor(s) of the identified patent applications to the above-identified assignee as shown on Exhibit A.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

CIRRUS LOGIC, INC.

10/09/01
Date _____
Stephanie A. Lucie
Vice President

EXHIBIT A

Serial No.	Attorney Docket No.	First Named Inventor	Title	Reel and Frame Number
09/865,079	M-11403 US	Trotter	Apparatus and Method for Multi-Channel Digital to Analog Conversion of Signals with Different Sample Rates	011859/0567
09/911,334	M-11406 US	Jensen, et al	High Speed Single Ended Sense Amplifier	Copy of Assignment Attached
08/994,559	M-11408-1C US	Schroeder, et al	User Interface with Improved Data Entry Features for Telephone Systems	7689/0504
09/054,542	M-11410 US	Kasha, et al	Analog to Digital Switched Capacitor Converter Using a Delta-Sigma Modulator Having Very Low Power, Distortion and Noise	9309/0667
09/827,396	M-11410-1C US	Kasha, et al	Analog to Digital Switched Capacitor Converter Using a Delta-Sigma Modulator Having Very Low Power, Distortion and Noise	9309/0667
09/393,093	M-11414 US	Munoz, et al	Successive Approximation Algorithm-Based Architecture and Systems	010436/0573
08/941,566	M-11433 US	Rhode	Digital-to Analog Converter with Power Up/Down Transient Suppression and Automatic Rate Switching	8836/0816
09/908,672	M-11433-2D US	Rhode	Digital-to Analog Converter with Power Up/Down	8836/0816

Serial No.	Attorney Docket No.	First Named Inventor	Title	Reel and Frame Number
			Transient Suppression and Automatic Rate Switching	
09/153,364	M-11434 US	Chow, et al	Power on Reset Techniques for an Integrated Circuit Chip	9625/0489
09/153,861	M-11435 US	Page, et al	Clock Alignment for Reduced Noise and Easy Interfacing	9617/0748
09/984,167	M-11664 US	Deans	Method and System for Creating Multi-Instanced Software with a Preprocessor	8910/0946
09/054,521	M-11666 US	Kasha, et al	Switched Capacitor Integrator Having Very Low Power and Low Distortion and Noise	9333/0556
09/387,636	M-11668-2C US	Chu	Booth Multiplier with Low Power, High Performance Input Circuitry	7325/0778
09/154,241	M-11669 US	Page, et al	Noise Management Using a Switched Converter	9617/0735
09/393,046	M-11738 US	Munoz, et al	Redundancy Methods and System Calibration and Conversion with Digital-to-Analog (DAC) Elements in an Analog-to-Digital (A/D) Converter	010566/0228
09/153,867	M-11740 US	Page, et al	System and Techniques for Seismic Data Acquisition	9625/0945
09/393,091	M-11829 US	Munoz, et al	Selection and Use of Bit Testing Sequences for Radix<2 A/D SAR Converters	010440/0577

Serial No.	Attorney Docket No.	First Named Inventor	Title	Reel and Frame Number
09/393,826	M-11965 US	Munoz, et al	System and Apparatus for Digitally Calibrating Capacitors in an Analog-to-Digital Converter Using Successive Approximation	010438/0034
09/153,862	M-11991 US	Page, et al	Sinc Filter with Selective Decimation Ratios	9636/0108
09/153,866	M-11992 US	Page, et al	Linear Phase Fir Sinc Filter with Multiplexing	9609/0823
09/153,860	M-11993 US	Page, et al	A Sinc Filter Using Twisting Symmetry	9620/0309
08/487,044	M-12127 US	Gardner	Continuous CDPD Base Station and Method of Facilitating Efficient Data Transfer	7635/0362
08/637,062	M-12128-1D US	Ecclesine	Network Controller Having Automatically Invoked DMA Data Transfer	7253/0807
09/049,214	M-12184 US	Lim, et al.	Methods And System To Transmit Data Acquired At A Variable Rate Over A Fixed Rate Channel	9124/0267
09/153,869	M-12292 US	Page, et al.	A Chip Architecture For Data Acquisition	9609/0399
09/154,242	M-12293 US	Page, et al.	Network Synchronization	9646/0090

Attorney Docket No.: M-11406 US

ASSIGNMENT

For good and valuable consideration, receipt of which is hereby acknowledged, we

Robert A. Jensen of Austin, Texas
Dimitris C. Pantelakis of Austin, Texas

hereby sell, assign and transfer to Cirrus Logic, Inc., a Delaware corporation, having a place of business at 4210 S. Industrial Drive, Austin, Texas 78744, its successors and assigns, the entire right, title and interest throughout the world in our invention in:

High Speed Single Ended Sense Amplifier

for which we have executed a United States patent application on or about the date of this assignment, and all patent applications and patents of every country for said invention, including divisions, reissues, continuations and extensions thereof, and all rights of priority resulting from the filing of said applications; we authorize the above-named assignee to apply for patents of foreign countries for said invention, and to claim all rights of priority without further authorization from us; we agree to execute all papers useful in connection with said United States and foreign applications, and generally to do everything possible to aid said assignee, their successors, assigns and nominees, at their request and expense, in obtaining and enforcing patents for said invention in all countries; and we request that the United States Patent and Trademark Office issue all patents granted for said invention to the above-named assignee, its successors and assigns.

Executed this 17th day of July, 2001.

Robert A. Jensen
 Robert A. Jensen

State of Texas)
) ss.
 County of Travis)

On July 17, 2001 before me, Crystal J. Levesque personally appeared Robert A. Jensen personally known to me or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity, and that by his signature on the instrument the persons, or the entity upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.



Crystal J. Levesque
 SIGNATURE OF NOTARY

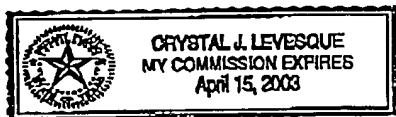
Executed this 17 day of July, 2001.

Dimitris C. Pantelakis
Dimitris C. Pantelakis

State of Texas)
) ss.
County of Travis)

On July 17, 2001 before me, Crystal J. Levesque
personally appeared Dimitris C. Pantelakis personally known to me or proved to me on the
basis of satisfactory evidence to be the person whose name is subscribed to the within
instrument and acknowledged to me that he executed the same in his authorized capacity, and
that by his signature on the instrument the persons, or the entity upon behalf of which the
person acted, executed the instrument.

WITNESS my hand and official seal.



Crystal J. Levesque
SIGNATURE OF NOTARY